

DVI Board for DLP™-Based Projector

ET-MD77DV

This input board is designed for use with the DLP™-based projectors. It enables the input of a digital signal (DVI-D) from a computer. Remote control of the projector is also possible from an external computer connected via LAN.

NOTE: Please use the module only on the specified Panasonic DLP™-based projector.

Specifications

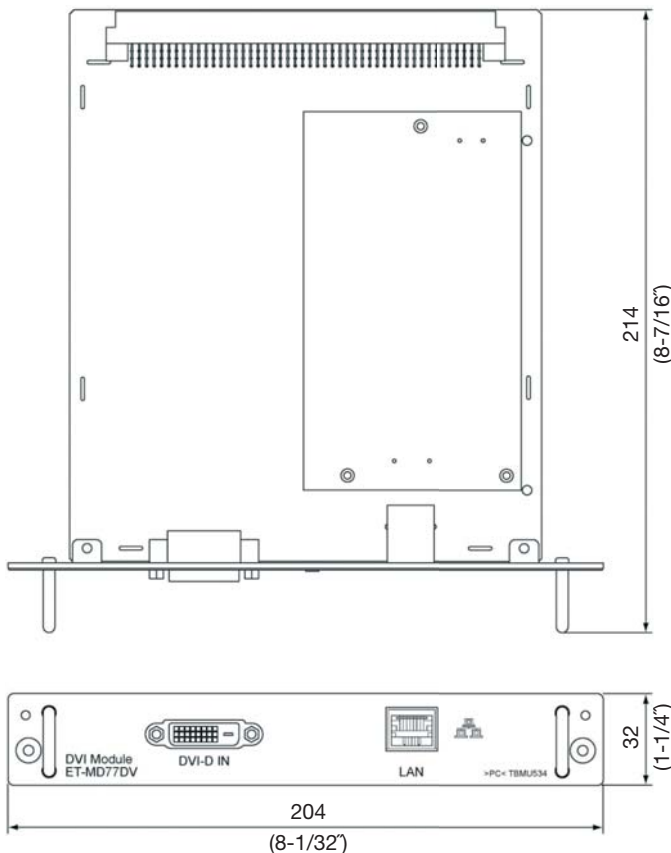
Power supply:	Provided from the DLP™-based projector
Power consumption:	Projector power consumption increases approximately 5 W
Terminals: DVI-D input:	DVI-D 24-pin x 1, DVI 1.0 compliant, compatible with HDCP
	EDID1: 480p, 576p, 720/60p, 720/50p, 1080/60i, 1080/50i, 1080/25p, 1080/24p, 1080/24sF, 1080/30p
	EDID2: VGA (640 x 480) – U-XGA (1,600 x 1,200), dot clock: 25 – 162 MHz
LAN:	RJ-45 x 1, 10Base-T/100Base-TX for network connection
Dimensions (W x H x D):	204 x 32 x 214 mm (8-1/32" x 1-1/4" x 8-7/16")
Weight:	310 g (10.9 lbs.)
Operating temperature:	0°C – 40°C
Operating humidity:	10% – 80% (no condensation)

NOTE: Ready for single link only. Ready for the non-interlaced signals only with EDID2. 576p and 720/50p signals can be input only when this unit is used with the PT-D7700 or PT-DW7000.

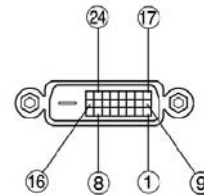
Weights and dimensions shown are approximate. Specifications subject to change without notice. Digital Light Processing and DLP are trademarks of Texas Instruments.

Dimensions

unit: mm



DVI-D output connector pin assignment



- | no. | signal |
|-----|----------------------------|
| 1 | T, M, D, S data 2- |
| 2 | T, M, D, S data 2+ |
| 3 | T, M, D, S data 2/4 shield |
| 4 | T, M, D, S data 4- |
| 5 | T, M, D, S data 4+ |
| 6 | DDC clock |
| 7 | DDC clock |
| 8 | NC |
| 9 | T, M, D, S data 1- |
| 10 | T, M, D, S data 1+ |
| 11 | T, M, D, S data 1/3 shield |
| 12 | T, M, D, S data 3- |
| 13 | T, M, D, S data 3+ |
| 14 | +5 V |
| 15 | GND |
| 16 | Hot plug detection |
| 17 | T, M, D, S data 0- |
| 18 | T, M, D, S data 0+ |
| 19 | T, M, D, S data 0/5 shield |
| 20 | T, M, D, S data 5- |
| 21 | T, M, D, S data 5+ |
| 22 | T, M, D, S clock shield |
| 23 | T, M, D, S clock + |
| 24 | T, M, D, S clock - |

Note: This illustration is not drawn to scale.